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PATENTS
104195-0010

SPECIFICATION AMENDMENT SCHEDULE:

replace paragraph on p.4 line 21  5/15/2008

As shown, platform 2 includes redundant switching buses 10a and 10b. Redundant CPU cards 12a, 12b are connected to buses 10a, 10b, an HDLC bus 14, and to host 4 by way of input/output (I/O) cards 16a, 16b. Depending upon the requirements of a particular application, various combinations of the following "line" cards, each of which supports a particular digital telecommunications protocol, may be included within platform 2: T1 card 16; E1 card 18, DS3 cards 20a, 20b. For redundancy, a standby card 22, which is a duplicate of one of the other line cards, may also be included. All such line cards have an associated I/O card, denoted collectively by reference number 26, which serves as an interface to PSTN 6.